

ABSTRACT OF THE DISCLOSURE

A discharge transistor (TFT3) which connects the upper
end of an organic EL element (EL) and a negative power source
5 (VEE) and a control transistor (TFT4) which connects the upper
end of a storage capacitor (SC) with a power source (PVDD) are
provided. These transistors (TFT3, TFT4) are turned on by the
upper gate line, so that the capacitor of the organic EL
element (EL) is discharged prior to the selection of the line
10 for these transistors.

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